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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/630,939	07/31/2003	Mitsuaki Osame	12732-161001	1228
26171	7590	01/26/2006		EXAMINER
FISH & RICHARDSON P.C. P.O. BOX 1022 MINNEAPOLIS, MN 55440-1022			VU, DAVID HUNG	
			ART UNIT	PAPER NUMBER
			2828	

DATE MAILED: 01/26/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/630,939	OSAME ET AL.	
	Examiner David Vu	Art Unit 2828	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 14 November 2005.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 7-12 and 19-38 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 7-12 and 19-38 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____.
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____.

DETAILED ACTION

Specification

1. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 7-12, 21-28, 30, 34-35, and 37 are rejected under 35 U.S.C. 102(e) as being anticipated by Koyama, U.S. Pat No 6,760,004.

Regarding claims 7-8, 10-11, Koyama discloses the claimed invention including a source line driving circuit 100; a first pixel comprising a first source line S1 and a first power source line V1; first TFT controlled by a video signal in the pixel portion; a gate line driving circuit inherently connected to gate signal lines G1; and a second pixel comprising a second source line S2 and a second power source line V2, wherein the source line driving circuit is electrically connected to the first source line and the second source line, wherein inherently a first power source is electrically connected to both the last stage and the first power source line, and wherein inherently a second power

source electrically connected to both the source line driving circuit and the second power source line, see, for example, figures 1-2, 4, column 2, lines 62+, column 3, lines 1-16, column 4, lines 10-19, column 6, lines 19-22, column 8, lines 25-27. Note that in a circuit all components are electrically connected together.

Regarding claims 9,12, inherently the power sources supply different or the same power so as to adjust brightness of each pixel.

Regarding claims 21-28, 34-35 , columns 27-28 discloses limitations, e.g., matrix light emitting device, video camera, digital camera etc, as claimed.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 19-20 and 31 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koyama in view of Yamazaki et al (herein after Yamazaki), U.S. Pub. No 2002/0018060.

Koyama as discussed from the above, essentially disclose the claimed invention but fail to explicitly disclose a level shifter. Yamazaki discloses a light emitting device with a level shifter circuit in the last stage, i.e., the stage after amplification (figure 3). It would have been obvious to one having ordinary skill in the art at the time of applicant's claimed invention was made to have provided the Koyama reference with the level

shifter so as to regulate the output to the source signal lines; thus, data signals would have been stabilized.

6. Claims 29, 32-33, 36, and 38 are rejected under 35 U.S.C. 103(a) as being unpatentable over Koyama in view of Chimura, U.S. Pub. No 2005/0012704.

Koyama as discussed from the above, essentially disclose the claimed invention including first TFT in the pixel portion controlled by a video signal (figures 3-4); a gate line driving circuit inherently connected to gate signal lines G1. Koyama fails to explicitly disclose the source line driving circuit comprising a second TFT connected to the source line. Chimura discloses source line driving circuit 12002 comprising a second TFT connected to the source line 12007 (figure 12, paragraph [005]). It would have been obvious to one having ordinary skill in the art at the time of applicant's claimed invention was made to have provided the Koyama reference with the source line driving circuit having a second TFT connected to the source line; thus, data signals supplied to the pixels would have been turned on or off.

Claims 32-33, columns 27-28 of the Koyama reference discloses limitations, e.g., active matrix light emitting device, video camera, digital camera etc, as claimed.

Regarding claim 38, in a circuit all elements are connected together; thus, the Koyama and Chimura combination clearly shows the particular connection as claimed.

Response to Arguments

7. Applicant's arguments filed 11/14/2005 have been fully considered but they are not persuasive.

Applicant's main arguments are centered around Koyama's power source is only electrically connected to the source signal line driving circuit since Koyama's pixel does not include a power source line as shown in Fig. 7. That the digital gradation signal VD is only connected to the source line driving circuit and there is no suggestion that it is connected to a power source line of a pixel. The pixel of the second design, as applicant alleged, is void of a power source line. The Examiner respectfully disagrees with these assertions because applicant mischaracterizes the Koyama reference. Koyama discloses a light emitting device including a source line driving circuit 100 (figure 1); a first pixel comprising a first source line S1 and a first power source line V1; and a second pixel comprising a second source line S2 and a second power source line V2 (figure 4), wherein the source line driving circuit is electrically connected to the first source line and the second source line, wherein inherently a first power source is electrically connected to both the source line driving circuit and the first power source line, and wherein inherently a second power source electrically connected to both the source line driving circuit and the second power source line. Furthermore, applicant's argument that there is no suggestion to combine the two embodiments has no merit since at least column 8, lines 1-29 disclose the combination of embodiments in figures 1-4.

Conclusion

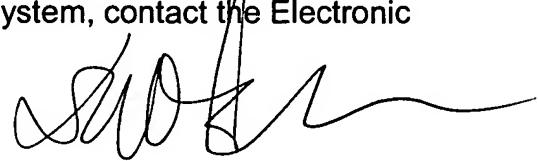
8. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Vu whose telephone number is (571) 272-1831. The examiner can normally be reached on M-F 8am-430pm.

The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



David Vu
Primary Examiner
Art Unit 2828